

SpyderControl Suite 2012 for Windows Vista, Windows 7, and Windows 8 Release Notes.

Upgrade note:

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If upgrading from a previous version, we strongly recommend performing a backup of the system data using the backup feature of the URS client software. Also, remove any previous versions of the product by using 'Add / Remove Programs' in the Windows control panel.

Legend:

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[+] new feature

[-] bug fixed or removed

[\*] bug fixed and improvement made

[N] general note

[K] known issue

Stability Index:

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The stability index provides a benchmark over the level of quality involved in a specific release or beta version of

software. Before installing any beta software versions, be sure to check the stability index to ensure.

Why make Alpha / Beta software available to users:

Vista products are defined and refined from customer feedback, and sharing pre-release software tightens the feedback loop, greatly increasing the user experience available in release versions.

Stability Index Scale (1-5)

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1: (Alpha) Released for testing of specific features. Software features may not be complete and may even contain severe issues or contain broken features or functionality. Users are strongly urged NOT to run this software in a production environment.

2: (Alpha) New features are considered to be complete or very close to completion. Little testing has been performed on the overall functionality of the build, and is not recommended for production use.

3: (Beta) Build has received a large amount of internal testing, and is considered to be in fair overall working condition.

This build is recommended for expert-level Spyder users interested in previewing and testing new functionality and fixes available since the last release version, in a non-production environment.

4: (Release Candidate) Software considered to be production ready. Software builds in this state receives extensive testing internally, and is recommended for intermediate to expert users interested in providing feedback on the build. In cases where specific issues have been resolved since the previous release, Vista may recommend the use of this software in a production environment. Manuals and other documentation may not yet be complete at this stage.

5: (Release) Software is considered production grade, and the software package is being distributed as an official release. This software is recommended for production use, and new units purchased are shipped with the latest release version. Be sure to read any applicable release notes, if available, before updating software to be informed of any 'breaking changes' or other notes.

#### Reporting Bugs or Requesting Features

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To let us know about a bug in an existing version of Vista software for any of our products, or to let us know what we can do to make the product even better, visit our online support links:

Contacts -- <http://www.vistasystems.net/contact.html>

Bug Reports -- <http://www.vistasystems.net/support.html>

Features -- <http://www.vistasystems.net/feature.html>

#### What's new in the Vista Control Suite 2012 Client Version 4.0.1 build 527 (11-27-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.11 Input\_CPLD-3.0 Output\_CPLD-4.0 Input\_Power-1.0 Output\_Power 1.0

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Stability Index: 5 (Release)

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1. [-] Resolved an issue from the previous beta where Spyder 200/300 VI height steppings were being enforced on Spyder X20 systems.
2. [-] Resolved an issue with layout changes causing a crash in Windows XP based clients

#### What's new in the Vista Control Suite 2012 Client Version 0.54.11 build 525 (10-31-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.11 Input\_CPLD-3.0 Output\_CPLD-4.0 Input\_Power-1.0 Output\_Power 1.0

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Stability Index: 3 (Beta)

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1. [-] Resolved a deployment problem in the previous beta (0.54.10) which prevented binaries from being upgraded during the update.

What's new in the Vista Control Suite 2012 Client Version 0.54.10 build 525 (10-31-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.11 Input\_CPLD-3.0 Output\_CPLD-4.0 Input\_Power-1.0 Output\_Power 1.0

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Stability Index: 3 (Beta)

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1. [-] Testing a fix for sync dropout problems related to certain X20 outputs when in HDCP mode (experimental).
2. [-] Fixed logic causing Sierra, RGB Linx, Quartz, and Extron IP routers to operate in 'real' mode when running in an offline session of Vista Advanced.
3. [-] Resolved a problem with the embedded Windows hard disk image on newer systems causing remote desktop connections to disconnect shortly after connection.
4. [\*] Added serial settings to property panel for Utah Scientific router.

What's new in the Vista Control Suite 2012 Client Version 0.54.9 build 524 (09-26-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1 Input\_CPLD-3.0 Output\_CPLD-4.0 Input\_Power-1.0 Output\_Power 1.0

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Stability Index: 3 (Beta)

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1. [-] Fixed issue with invalid X20 internal routing crosspoints being set when using linear key inputs in preview on slave frames of a parallel system (experimental).
2. [\*] In parallel X20 configurations, when opening LLDiag (internal low-level diagnostics) from the server, a dialog will open asking for a USB connection index to attach to.

3. [-] Fixed issue where renaming function keys or command keys from the property panel would result cause an application crash.

What's new in the Vista Control Suite 2012 Client Version 0.54.8 build 523 (09-06-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1  
Input\_CPLD-3.0 Output\_CPLD-4.0 Input\_Power-1.0 Output\_Power 1.0

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Stability Index: 3 (Beta)

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1. [-] Fixed issue in X20 preview where only the first 4 preview layers on each input board were appearing in preview (Experimental).
2. [+] Added '8-Bit RGB Checkerboard' test pattern to the 'Color Field' pattern category. This is available for layers, outputs, and PixelSpaces.

What's new in the Vista Control Suite 2012 Client Version 0.54.7 build 522 (08-28-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.13.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1  
Input\_CPLD-3.0 Output\_CPLD-4.0 Input\_Power-1.0 Output\_Power 1.0

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Stability Index: 3 (Beta)

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1. [+] Added external control option 'n' / 'normalize' to external control command RLK to cause the absolute rectangle of the layer to be scaled to compensate for the size of a preview PixelSpace.
2. [\*] If a layer was not visible, the external control command RLK would report a PixelSpace ID of -1. This behavior has been modified to show the actual pixelspace; users should additionally check the transparency response argument to validate if the layer is on screen.
3. [-] Fixed bug in 0.54.5 beta (not released to web) where the 'AutoSelectHeight' flag in SystemSettings.xml was being processed inverse of it's set value.
4. [-] Linear key was failing to appear correctly if the cut / fill signals were coming from different input boards on an X20 1608 system.

What's new in the Vista Control Suite 2012 Client Version 0.54.5 build 521 (08-20-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1  
Input\_CPLD-3.0 Output\_CPLD-4.0 Input\_Power-1.0 Output\_Power 1.0

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Stability Index: 3 (Beta)  
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1. [-] Linear key was failing to patch properly for preview layers on X20 expanded frames.
2. [-] Alert viewer in Advanced was resetting it's tracing level to warning after 30 seconds even if a user modified the tracing level immediately after startup.
3. [+] Added support for different VI heights between multiple X20 frames connected via USB expansion.
4. [\*] Switched the Spyder Control Suite 2012 installer to use install shield (experimental).

What's new in the Vista Control Suite 2012 Client Version 0.54.4 build 520 (07-03-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1  
Input\_CPLD-3.0 Output\_CPLD-4.0 Input\_Power-1.0 Output\_Power 1.0

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Stability Index: 3 (Beta)  
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1. [+] Added 'Gefen 3' router type, supporting newer firmware that switches the inputs and output ordering of switch commands.
2. [-] Fixed issue in external control 'RRL' command that would cause an internal error when a register with a null name existed.

What's new in the Vista Control Suite 2012 Client Version 0.54.3 build 519 (06-20-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1  
Input\_CPLD-3.0 Output\_CPLD-4.0 Input\_Power-1.0 Output\_Power 1.0

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Stability Index: 2 (Alpha)

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1. [-] Function Key and Command Key grid's copy operations not working in the previous beta.
  2. [-] Active stereo sources recalled to program using a command key would not recall the alternate eye correctly in some cases.
  3. [+] Added external control command 'AIR' to allow for layer resizing horizontally or vertically without adjusting the other axis.

What's new in the Vista Control Suite 2012 Client Version 0.54.2 build 518 (06-05-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1  
Input\_CPLD-3.0 Output\_CPLD-4.0 Input\_Power-1.0 Output\_Power 1.0

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Stability Index: 3 (Beta)

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1. [+] Added support for Atlona router.
  2. [-] Refactored register data list management to prevent a scenario where register lists can become corrupted, requiring a 'Data List Repair' (Experimental).

What's new in the Vista Control Suite 2012 Client Version 0.54.1 build 517 (05-21-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1  
Input\_CPLD-3.0 Output\_CPLD-4.0 Input\_Power-1.0 Output\_Power 1.0

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Stability Index: 4 (Release Candidate)

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1. [-] Fixed a few areas still referencing to the client software as Vista Advanced 2009 instead of 2012.
  2. [-] Fixed an issue where a 24Hz or 30Hz VI was not being configured correctly, causing the VI to actually run at NTSC.
  3. [-] Stills with a resolution wider than 2048 or taller than 1200 would appear in the simulator as a dual-layer source when in virtual (offline) mode.
  4. [\*] When an X20 layer is selected and no source monitor is configured in the system, a notification icon now appears next to the capture button in source properties.

5. [\*] An ArgumentException was being logged on the client intermittently when listening to server notification traffic.

6. [-] Fixed issue where keyframes were being lost when converting a mixer element to a source element in a script.

What's new in the Vista Control Suite 2012 Client Version 0.54.0 build 516 (04-27-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1  
Input\_CPLD-3.0 Output\_CPLD-4.0 Input\_Power-1.0 Output\_Power 1.0

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Stability Index: 4 (Release Candidate)

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1. [+] Added 'Request Register Details' (RRD) to external control protocol.
2. [+] Updated the 'Layer Clone Control' (LCC) to accept a raw pixel offset for the clone offset in the external control protocol.

What's new in the Vista Control Suite 2012 Client Version 4.0.0 build 515 (03-08-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1  
Input\_CPLD-3.0 Output\_CPLD-4.0 Input\_Power-1.0 Output\_Power 1.0

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Stability Index: 5 (Release)

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1. [\*] Re-packaging previous beta (0.53.3) into release version 4.0.0.