SpyderControl Suite 2012 for Windows Vista, Windows 7, and Windows 8 Release Notes.

Upgrade note:

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If upgrading from a previous version, we strongly recommend performing a backup of the system data using the backup feature of the URS client software. Also, remove any previous versions of the product by using 'Add / Remove Programs' in the Windows control panel.

Legend:

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[+] new feature

[-] bug fixed or removed

[*] bug fixed and improvement made

[N] general note

[K] known issue

Stability Index:

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The stability index provides a benchmark over the level of quality involved in a specific release or beta version of software. Before installing any beta software versions, be sure to check the stability index to ensure

Why make Alpha / Beta software available to users:

  Vista products are defined and refined from customer feedback, and sharing pre-release software tightens the feedback loop, greatly increasing the user experience available in release versions.

  Stability Index Scale (1-5)

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  1: (Alpha) Released for testing of specific features. Software features may not be complete and may even contain severe issues or contain broken features or functionality. Users are strongly urged NOT to run this software in a production environment.

  2: (Alpha) New features are considered to be complete or very close to completion. Little testing has been performed on the overall functionality of the build, and is not recommended for production use.

  3: (Beta) Build has received a large amount of internal testing, and is considered to be in fair overall working condition.

This build is recommended for expert-level Spyder users interested in previewing and testing new functionality and fixes available since the last release version, in a non-production environment.

4: (Release Candidate) Software considered to be production ready. Software builds in this state receives extensive testing internally, and is recommended for intermediate to expert users interested in providing feedback on the build. In cases where specific issues have been resolved since the previous release, Vista may recommend the use of this software in a production environment. Manuals and other documentation may not yet be complete at this stage.

5: (Release) Software is considered production grade, and the software package is being distributed as an official release. This software is recommended for production use, and new units purchased are shipped with the latest release version. Be sure to read any applicable release notes, if available, before updating software to be informed of any 'breaking changes' or other notes.

Reporting Bugs or Requesting Features

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To let us know about a bug in an existing version of Vista software for any of our products, or to let us know what we can do to make the product even better, visit our online support links:

Contacts -- http://www.vistasystems.net/contact.html

Bug Reports -- http://www.vistasystems.net/support.html

Features -- http://www.vistasystems.net/feature.html

What's new in the Vista Advanced 2012 Client Version 4.0.4 build 547 (05-13-2014)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-6.3.7.2 FRC-3.12.2.42 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.2 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 5 (Release)

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1. [+] Added Spyder X20 support for SDI Level B on inputs.

2. [+] Added support for the PN1 Pesa Router protocol over IP

3. [+] Added ability to display a Frame name in the Connection Manager and set it in the Frame Configuration

4. [-] Fixed bug with the names of Pixelspaces not updating in the bottom bar of the simulator

5. [-] Fixed bug that was causing the EDID Wizard to fail on the first try

6. [-] Fixed bug with setting refresh rates for outputs

7. [-] Removed the router remove button from the router salvo properties to avoid accidental removal

8. [-] Fixed bug with scrolling in the source register list

9. [-] Fixed bug with changing a sources input number affecting to connector type of adjacent source

10. [-] Fixed bug that was causing the still server connection status not to update

11. [-] Fixed bug with selecting outputs in the router salvo for large routers

12. [-] Fixed bug that was not setting the Pixelspace background to transparent when the user chooses No Background

What's new in the Vista Advanced 2012 Client Version 0.57.2 build 546 (03-13-2014)

==

Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.11.2.40 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.2 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

==

Stability Index: 3 (Beta)

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1. [-] Fixed a bug with firmware that was causing banding when showing gradients on a SDI connection

What's new in the Vista Advanced 2012 Client Version 0.57.1 build 546 (03-06-2014)

==

Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.11.2.39 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.2 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

==

Stability Index: 3 (Beta)

==

1. [+] Added support for the Pesa Cougar Router P1N protocol

2. [-] Fixed a bug with the Quartz protocol that could be causing issues with controlling a Magnum

3. [-] Fixed a bug with applying EDIDs that was causing it to fail the first try

4. [-] Changed the Finish button to say Cancel in the Parallel Configuration Wizard because it was misleading

5. [-] Updated feedback link to point to the new feedback website

What's new in the Vista Advanced 2012 Client Version 4.0.3 build 545 (02-04-2014)

==

Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.11.2.39 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.2 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

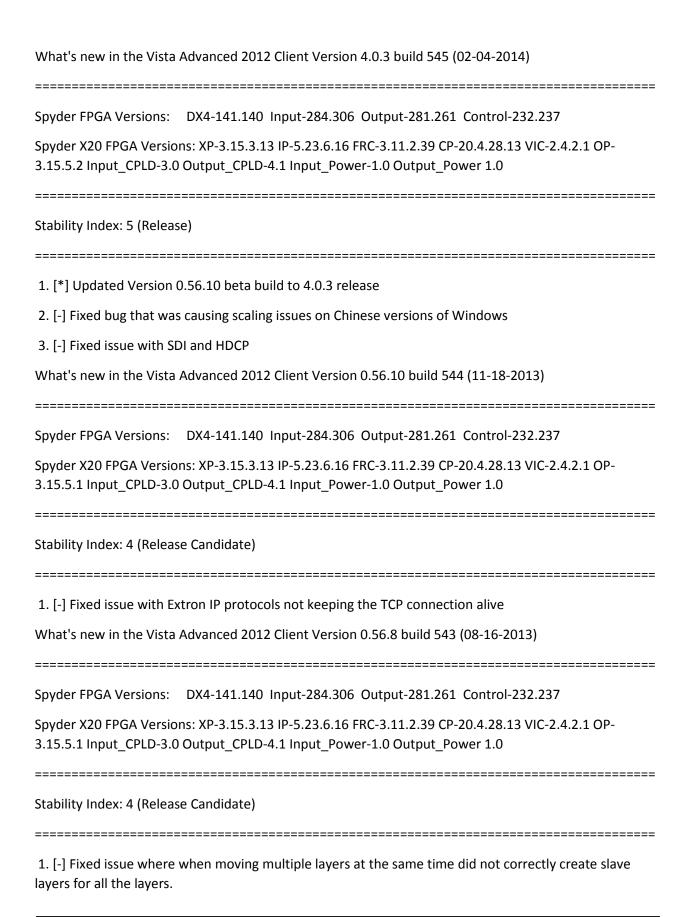==

Stability Index: 5 (Release)

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 1. [*] Updated Version 0.56.10 beta build to 4.0.3 release

 2. [-] Fixed bug that was causing scaling issues on Chinese versions of Windows

 3. [-] Fixed issue with SDI and HDCP

What's new in the Vista Advanced 2012 Client Version 0.56.10 build 544 (11-18-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.11.2.39 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 4 (Release Candidate)

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 1. [-] Fixed issue with Extron IP protocols not keeping the TCP connection alive

What's new in the Vista Advanced 2012 Client Version 0.56.8 build 543 (08-16-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.11.2.39 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 4 (Release Candidate)

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 1. [-] Fixed issue where when moving multiple layers at the same time did not correctly create slave layers for all the layers.

What's new in the Vista Control Suite 2012 Client Version 0.56.7 build 542 (07-31-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.10.2.39 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 4 (Release Candidate)

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 1. [-] Fixed issue where Spyder 200/300 VI height was being oversized in some circumstances.

 2. [-] Command key renaming on pages other than page 1 would cause the corresponding register ID on page 1 to become corrupted or disappear.

What's new in the Vista Control Suite 2012 Client Version 0.56.6 build 541 (07-26-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.10.2.39 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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 1. [-] Fixed issue where linear key layers may not appear correctly when certain source monitor configurations are in use.

 2. [-] Fixed issue with SDI input edges in X20 showing a blue/brown/black line along one or more edges.

 3. [-] The external control command LSP was not working correctly when used in conjunction with layers on pixelspaces with an X/Y offset greater than zero.

 4. [-] The UDP Console Simulator form would cause the Advanced interface to hang if a server connection could not be established upon form load.

What's new in the Vista Control Suite 2012 Client Version 0.56.5 build 540 (07-22-2013)

==

Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.8.2.39 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 2 (Alpha)

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 1. [-] Fixed issue in previous beta where transparency changes on a primary eye stereo source were not being pushed to the alternate eye.

What's new in the Vista Control Suite 2012 Client Version 0.56.4 build 539 (07-21-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.8.2.39 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 2 (Alpha)

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 1. [-] Fixed intermittent layer management problem where switching between stereoscopic X20 inputs and 2D content would cause incorrect results (experimental).

 2. [-] Fixed a scenario where all layers could stop transitioning momentarily while any layer was transitioning between PVW/PGM pixelspaces (X20 issue only) - (experimental).

What's new in the Vista Control Suite 2012 Client Version 0.56.3 build 538 (07-18-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.8.2.39 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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 1. [-] System configuration files for server packages (both X20 and 200/300 series Spyder) was incorrectly packaged in the previous beta.

What's new in the Vista Control Suite 2012 Client Version 0.56.2 build 538 (07-17-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.8.2.39 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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 1. [+] Added an IP implementation of the Gefen router protocol (Gefen IV).

 2. [-] Updated response parsing from Lightware protocol to support routers with slightly different line endings in their response.

 3. [-] Dropping a stereo source on to a PixelSpace could cause layers to be overwritten in some cases.

 4. [-] CPLD version information wasn't included in the previous beta (0.56.1) version file.

What's new in the Vista Control Suite 2012 Client Version 0.56.1 build 537 (07-15-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.8.2.39 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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 1. [*] Increased the server alert viewer's startup filtering timer to 60 seconds.

 2. [-] Fixed issue where 200/300 series Spyder systems could show noise on the VI when a layer is positioned at certain locations off the right side of the VI.

 3. [-] Removed an increased delay in autosync on X20 Spyder that appeared in the 4.0.2 release.

What's new in the Vista Control Suite 2012 Client Version 0.56.0 build 536 (07-03-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.7.2.38 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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1. [+] Removed 20 page limit on command key and function key panels.

2. [-] Auto sync on timing change would stop working after the first autosync it performed, until a config was explicitly applied.

3. [-] Cancel button on output property panel wasn't working correctly.

4. [-] Input configuration for stereo and dual-link inputs wasn't being updated on the alternate layer during an autosync.

What's new in the Vista Control Suite 2012 Client Version 4.0.2 build 535 (06-17-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.7.2.38 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 5 (Release)

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1. [*] Updated Version 0.55.9 beta build to 4.0.2 release.

What's new in the Vista Control Suite 2012 Client Version 0.55.9 build 534 (06-08-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.7.2.38 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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1. [+] The repair register list menu option now checks for the presence of 8 device definitions and creates them when missing.

2. [-] Fixed issue where passive stereo input connected via a router upstream of an X20 crosspoint would not patch properly (experimental).

3. [-] Fixed a couple remaining issues with scaling the Advanced application on different DPI displays.

What's new in the Vista Control Suite 2012 Client Version 0.55.8 build 533 (05-06-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.7.2.38 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1
Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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1. [+] Added optional gamma parameter in the existing Input Level Adjustment (ILA) external control command.

2. [-] Fixed UI scaling problems that would occur on high DPI computers running Vista Advanced.

What's new in the Vista Control Suite 2012 Client Version 0.55.7 build 532 (05-02-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.7.2.38 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1
Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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1. [+] Added external control response code and option to retrieve additional message data beyond 1400 bytes in a single response.

2. [+] Added external control command 'Request Image File' (RIF).

3. [+] Added external control command 'Request Script CueData Count' (RSCC).

4. [+] Added external control command 'Request Script Element Count' (RSEC).

5. [+] Added external control command 'Request Script CueData Details' (RSCD).

6. [-] Fixed an internal error causing the 'absolute' flag to not be propagated between command keys and their underlying scripts.

7. [+] Enhanced the 'Remove Unused Scripts' menu option (Tools Menu) to remove excessive extra CueDatas from scripts.

8. [+] Output frame lock status checkbox in the output configuration property panel now updates every few seconds instead of just when opening the panel.

9. [-] Output frame lock status changes were causing the output configuration property panel to show the 'Save Required' message.

10. [+] Added support for the DTrovision PureLink PM-32X router (IP and Serial).

What's new in the Vista Control Suite 2012 Client Version 0.55.6 build 531 (04-04-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.7.2.38 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

==

 1. [-] Fixed issue that would cause slave layers to intermittently remain in preview a command key recall (experimental).

What's new in the Vista Control Suite 2012 Client Version 0.55.5 build 530 (04-01-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.7.2.38 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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 1. [-] Fixed application lockup that would occur in 0.55.4 when advanced had multiple command key pages.

 2. [-] NVision Compact router was being initialized when running Advanced in offline mode.

 3. [*] Changed X20 HDCP output enumeration to be separated by frame boundaries to reduce the number of connected devices reported to inputs.

What's new in the Vista Control Suite 2012 Client Version 0.55.4 build 529 (03-26-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.7.2.38 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

==

Stability Index: 3 (Beta)

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1. [-] Patched issue where register names could get out of sync with underlying command key names, by adding validation checks when the Register lists are requested by clients.

2. [-] Fixed incorrect tooltip text in the Frame Settings panel under the AutoSync section.

What's new in the Vista Control Suite 2012 Client Version 0.55.3 build 528 (02-28-2013)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.7.2.38 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

==

1. [-] Added fix for stale data being displayed in X20 inputs where sync is detected without any new video (experimental).

What's new in the Vista Control Suite 2012 Client Version 0.55.2 build 528 (02-21-2013)

==

Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.4.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

==

Stability Index: 3 (Beta)

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1. [-] Fixed issue where X20 frames running stereo in parallel would not keep the left-eye/right-eye correlation between reboots (experimental). Requires genlock and a 50/59.94/60Hz VI configuration.

What's new in the Vista Control Suite 2012 Client Version 0.55.1 build 528 (12-20-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.5.1 Input_CPLD-3.0 Output_CPLD-4.1 Input_Power-1.0 Output_Power 1.0

==

Stability Index: 3 (Beta)

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1. [-] Fixed deployment problem with output binary in 0.55.0 beta.

2. [*] Updated output board firmware to support newer production power supplies.

What's new in the Vista Control Suite 2012 Client Version 0.55.0 build 528 (12-19-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

==

Stability Index: 3 (Beta)

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1. [+] Added support for AJA Kumo IP router.

2. [+] Added support for Utah Scientific RCP-3 Protocol based routers.

3. [-] Fixed issue where blend overlay wasn't being rotated correctly when an X20 output was rotated and running in active stereo mode.

4. [-] Updated output firmware in X20 to resolve problem with artifacts appearing on odd numbered outputs in some scenarios (experimental).

What's new in the Vista Control Suite 2012 Client Version 4.0.1 build 526 (11-19-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.11 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

==

Stability Index: 5 (Release)

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1. [-] Resolved an issue from the previous beta where Spyder 200/300 VI height steppings were being enforced on Spyder X20 systems.

2. [-] Resolved an issue with layout changes causing a crash in Windows XP based clients

What's new in the Vista Control Suite 2012 Client Version 0.54.11 build 525 (10-31-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.11 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

==

Stability Index: 3 (Beta)

==

 1. [-] Resolved a deployment problem in the previous beta (0.54.10) which prevented binaries from being upgraded during the update.

What's new in the Vista Control Suite 2012 Client Version 0.54.10 build 525 (10-31-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.11 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

==

Stability Index: 3 (Beta)

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 1. [-] Testing a fix for sync dropout problems related to certain X20 outputs when in HDCP mode (experimental).

 2. [-] Fixed logic causing Sierra, RGB Linx, Quartz, and Extron IP routers to operate in 'real' mode when running in an offline session of Vista Advanced.

 3. [-] Resolved a problem with the embedded Windows hard disk image on newer systems causing remote desktop connections to disconnect shortly after connection.

 4. [*] Added serial settings to property panel for Utah Scientific router.

What's new in the Vista Control Suite 2012 Client Version 0.54.9 build 524 (09-26-2012)

==

Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

==

Stability Index: 3 (Beta)

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 1. [-] Fixed issue with invalid X20 internal routing crosspoints being set when using linear key inputs in preview on slave frames of a parallel system (experimental).

2. [*] In parallel X20 configurations, when opening LLDiag (internal low-level diagnostics) from the server, a dialog will open asking for a USB connection index to attach to.

3. [-] Fixed issue where renaming function keys or command keys from the property panel would result cause an application crash.

What's new in the Vista Control Suite 2012 Client Version 0.54.8 build 523 (09-06-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.15.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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1. [-] Fixed issue in X20 preview where only the first 4 preview layers on each input board were appearing in preview (Experimental).

2. [+] Added '8-Bit RGB Checkerboard' test pattern to the 'Color Field' pattern category. This is available for layers, outputs, and PixelSpaces.

What's new in the Vista Control Suite 2012 Client Version 0.54.7 build 522 (08-28-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.13.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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1. [+] Added external control option 'n' / 'normalize' to external control command RLK to cause the absolute rectangle of the layer to be scaled to compensate for the size of a preview PixelSpace.

2. [*] If a layer was not visible, the external control command RLK would report a PixelSpace ID of -1. This behavior has been modified to show the actual pixelspace; users should additionally check the transparency response argument to validate if the layer is on screen.

3. [-] Fixed bug in 0.54.5 beta (not released to web) where the 'AutoSelectHeight' flag in SystemSettings.xml was being processed inverse of it's set value.

4. [-] Linear key was failing to appear correctly if the cut / fill signals were coming from different input boards on an X20 1608 system.

What's new in the Vista Control Suite 2012 Client Version 0.54.5 build 521 (08-20-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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 1. [-] Linear key was failing to patch properly for preview layers on X20 expanded frames.

 2. [-] Alert viewer in Advanced was resetting it's tracing level to warning after 30 seconds even if a user modified the tracing level immediately after startup.

 3. [+] Added support for different VI heights between multiple X20 frames connected via USB expansion.

 4. [*] Switched the Spyder Control Suite 2012 installer to use install shield (experimental).

What's new in the Vista Control Suite 2012 Client Version 0.54.4 build 520 (07-03-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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 1. [+] Added 'Gefen 3' router type, supporting newer firmware that switches the inputs and output ordering of switch commands.

 2. [-] Fixed issue in external control 'RRL' command that would cause an internal error when a register with a null name existed.

What's new in the Vista Control Suite 2012 Client Version 0.54.3 build 519 (06-20-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

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Stability Index: 2 (Alpha)

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 1. [-] Function Key and Command Key grid's copy operations not working in the previous beta.

 2. [-] Active stereo sources recalled to program using a command key would not recall the alternate eye correctly in some cases.

 3. [+] Added external control command 'AIR' to allow for layer resizing horizontally or vertically without adjusting the other axis.

What's new in the Vista Control Suite 2012 Client Version 0.54.2 build 518 (06-05-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

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Stability Index: 3 (Beta)

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 1. [+] Added support for Atlona router.

 2. [-] Refactored register data list management to prevent a scenario where register lists can become corrupted, requiring a 'Data List Repair' (Experimental).

What's new in the Vista Control Suite 2012 Client Version 0.54.1 build 517 (05-21-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

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Stability Index: 4 (Release Candidate)

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 1. [-] Fixed a few areas still referencing to the client software as Vista Advanced 2009 instead of 2012.

 2. [-] Fixed an issue where a 24Hz or 30Hz VI was not being configured correctly, causing the VI to actually run at NTSC.

3. [-] Stills with a resolution wider than 2048 or taller than 1200 would appear in the simulator as a dual-layer source when in virtual (offline) mode.

4. [*] When an X20 layer is selected and no source monitor is configured in the system, a notification icon now appears next to the capture button in source properties.

5. [*] An ArgumentException was being logged on the client intermittently when listening to server notification traffic.

6. [-] Fixed issue where keyframes were being lost when converting a mixer element to a source element in a script.

What's new in the Vista Control Suite 2012 Client Version 0.54.0 build 516 (04-27-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

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Stability Index: 4 (Release Candidate)

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1. [+] Added 'Request Register Details' (RRD) to external control protocol.

2. [+] Updated the 'Layer Clone Control' (LCC) to accept a raw pixel offset for the clone offset in the external control protocol.

What's new in the Vista Control Suite 2012 Client Version 4.0.0 build 515 (03-08-2012)

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Spyder FPGA Versions: DX4-141.140 Input-284.306 Output-281.261 Control-232.237

Spyder X20 FPGA Versions: XP-3.12.3.13 IP-5.23.6.16 FRC-3.6.2.38 CP-20.4.28.13 VIC-2.3.2.1 OP-3.15.4.1 Input_CPLD-3.0 Output_CPLD-4.0 Input_Power-1.0 Output_Power 1.0

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Stability Index: 5 (Release)

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1. [*] Re-packaging previous beta (0.53.3) into release version 4.0.0.